

This Page Is Inserted by IFW Operations  
and is not a part of the Official Record

## BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
-  BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning documents *will not* correct images,  
please do not report the images to the  
Image Problem Mailbox.**

Control Circuit of MOSFET For Synchronous Rectification

Yukihiro Nishikawa  
U.S. Serial No. 10/609,341  
1/15

MAY 18 2004

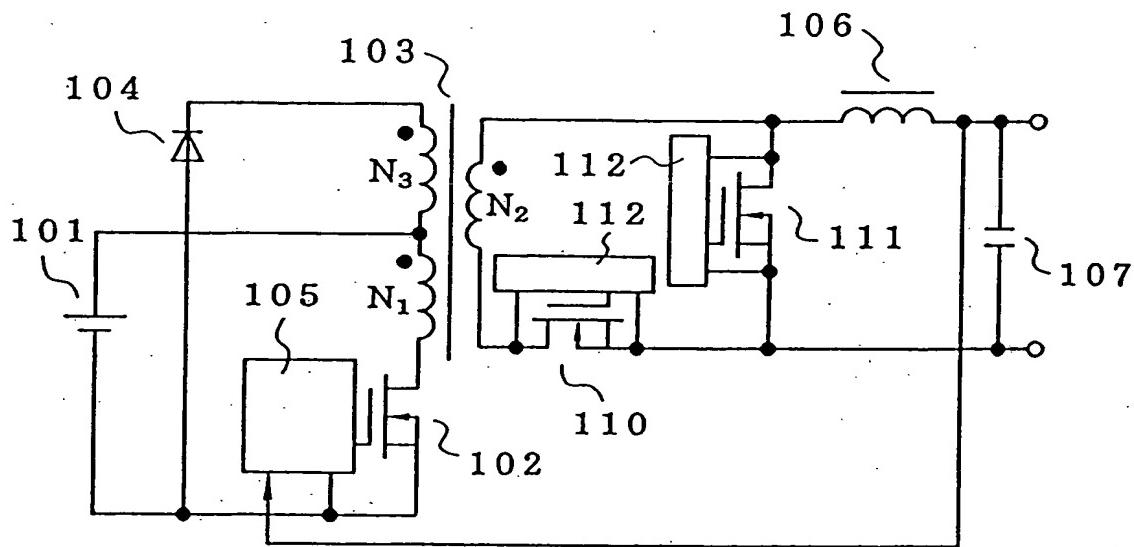


FIG. 1

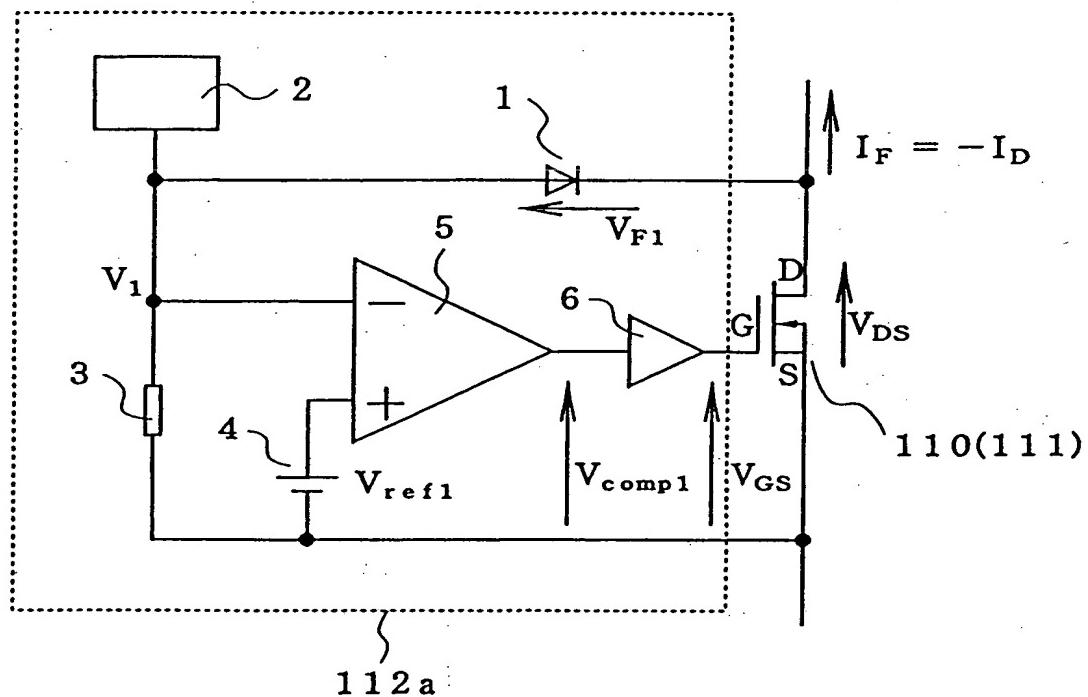


FIG. 2

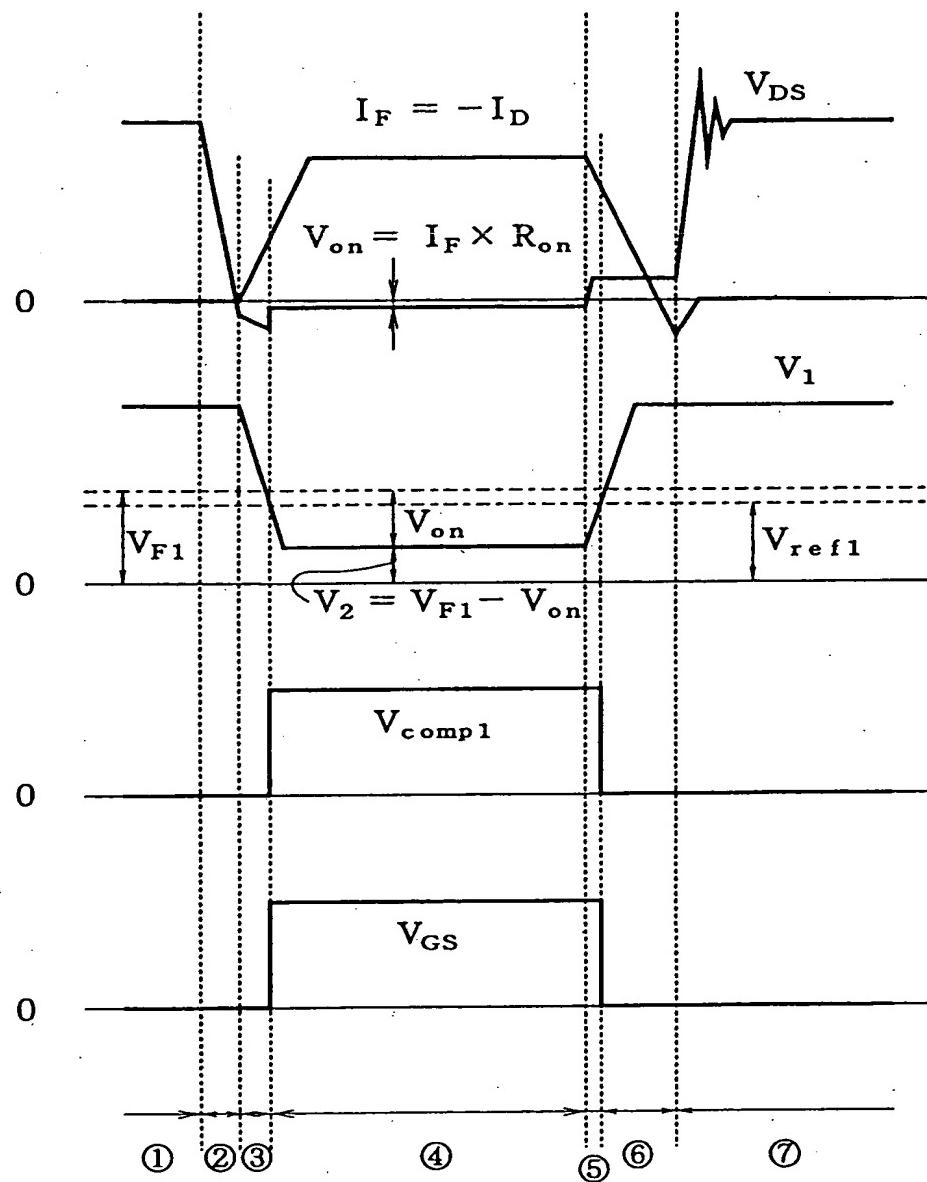


FIG. 3

FIG. 4

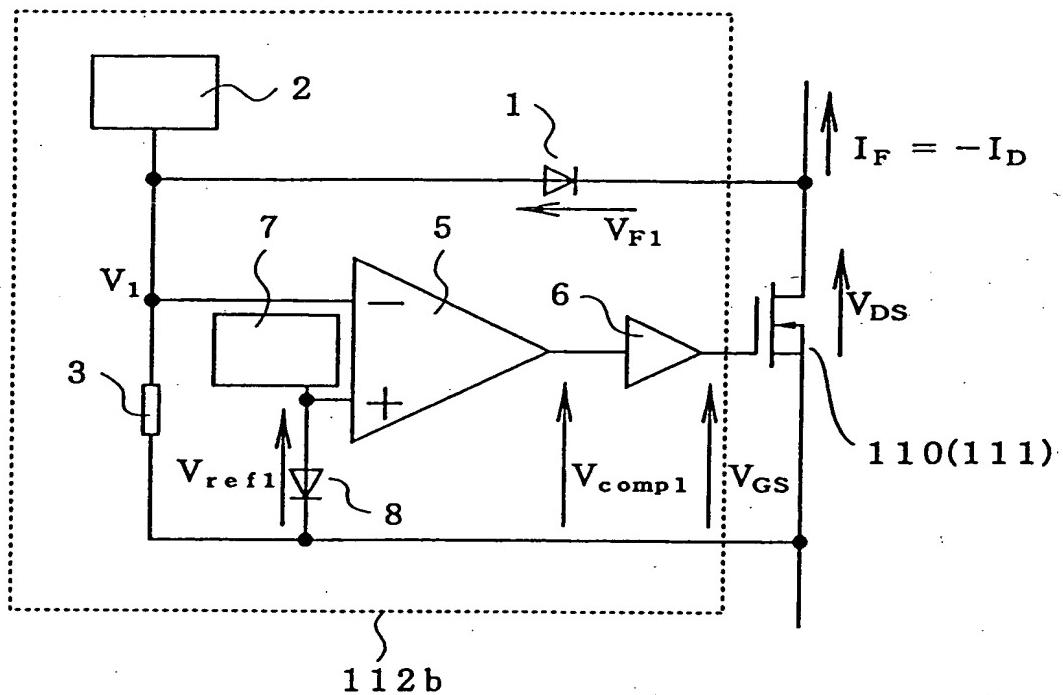
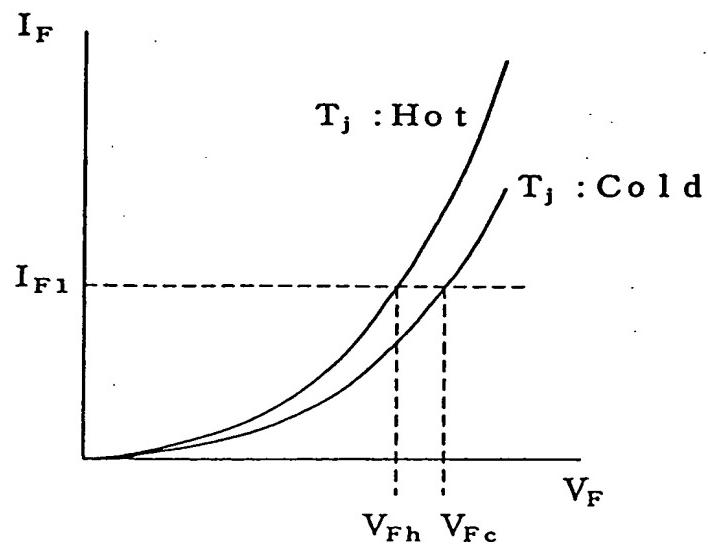


FIG. 5



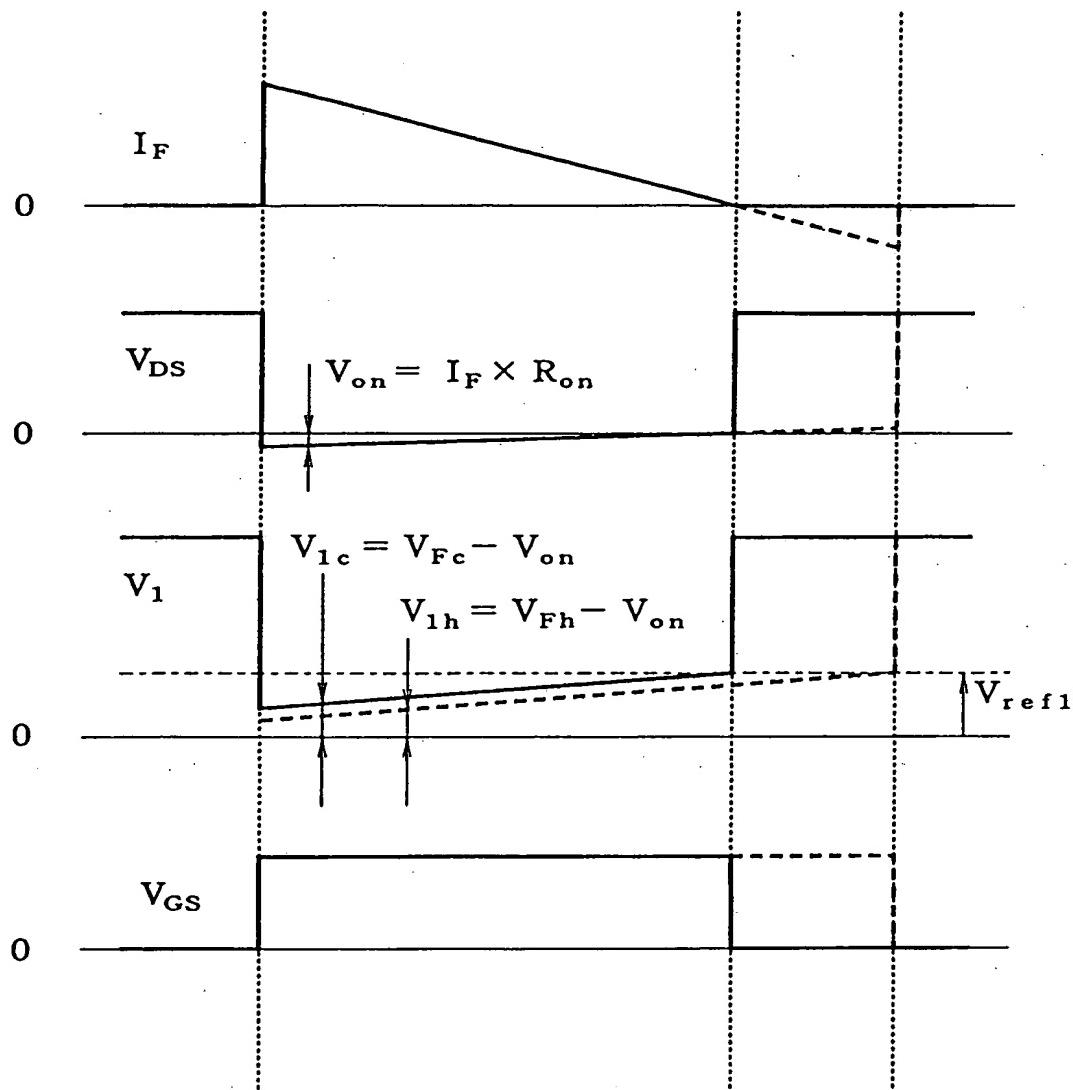


FIG. 6

FIG. 7

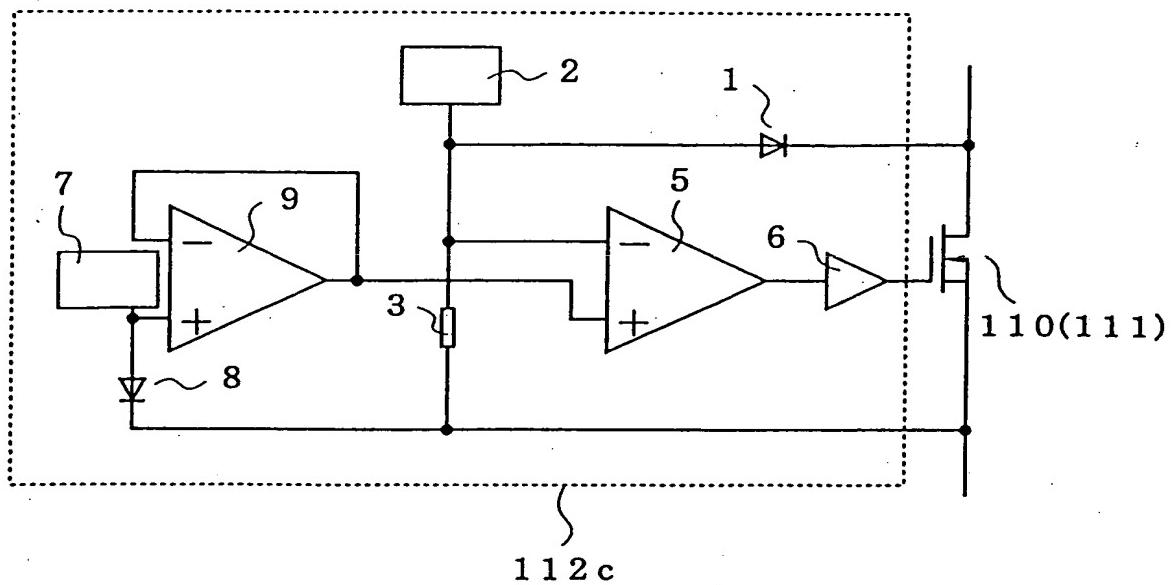


FIG. 8

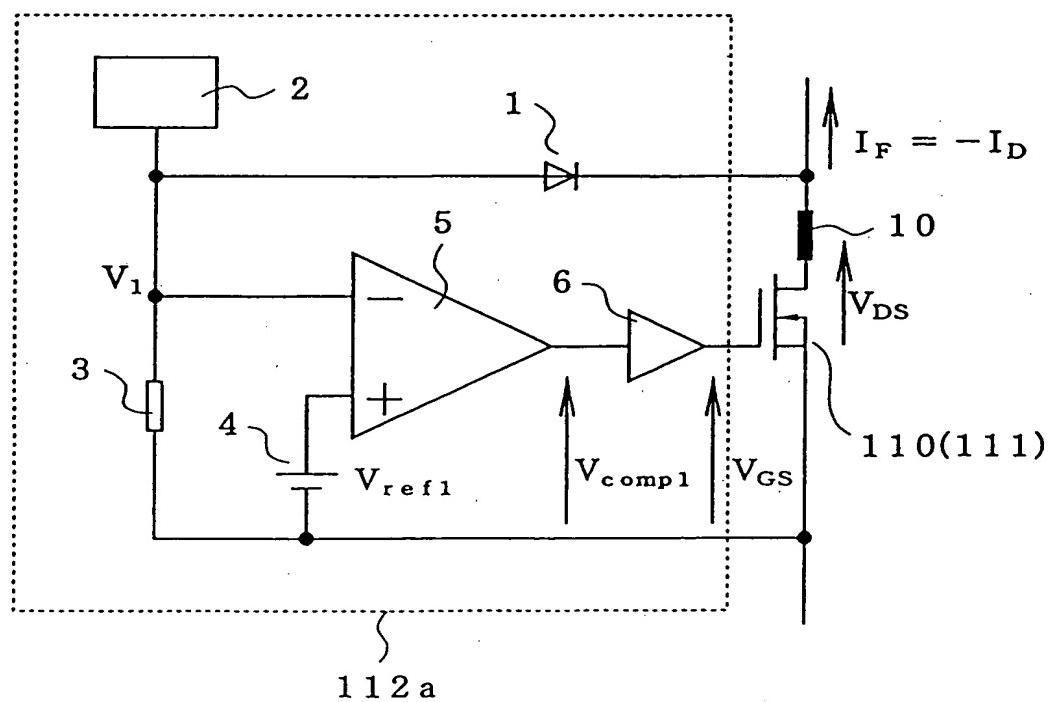


FIG. 9

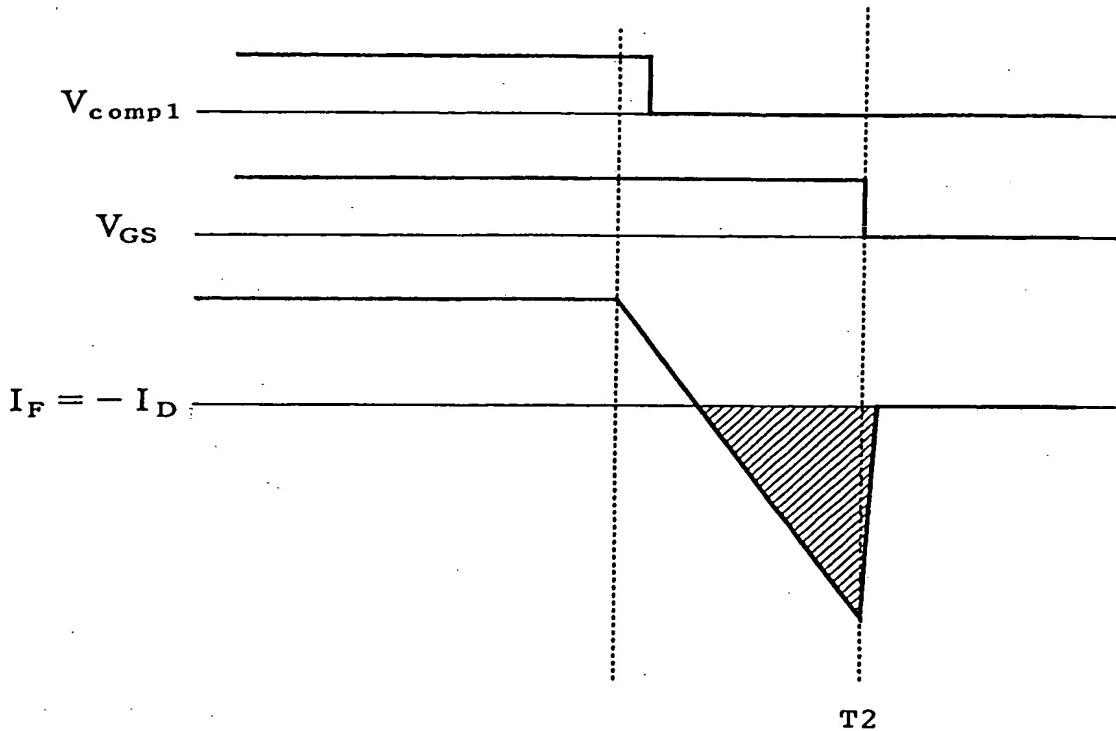
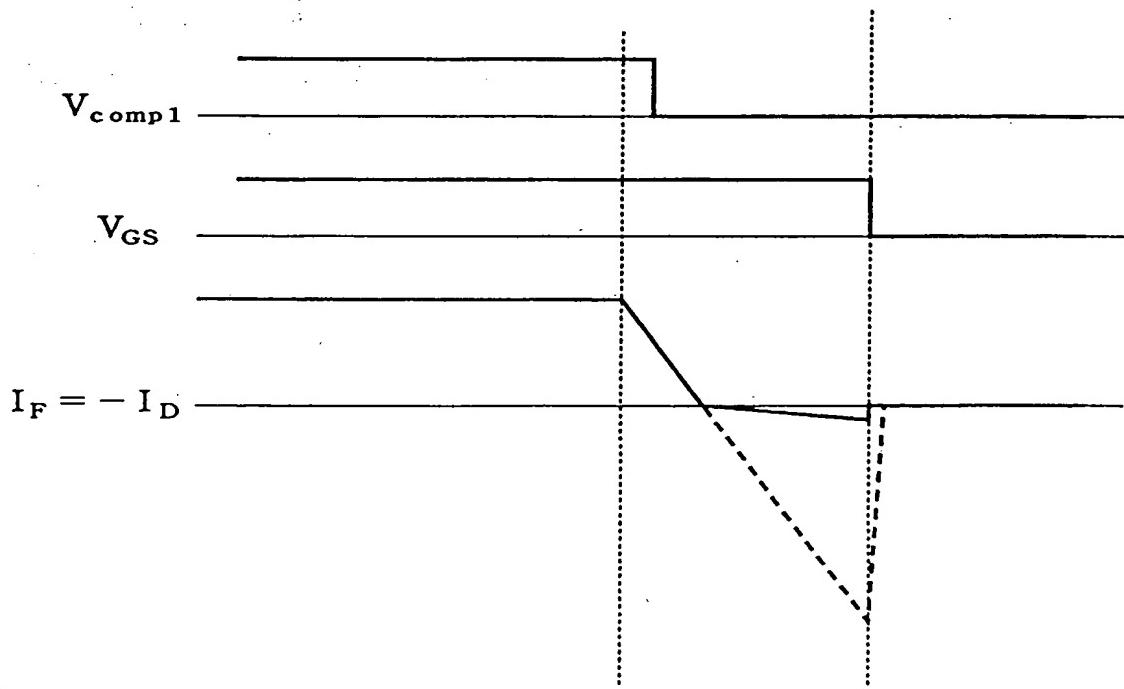


FIG. 10



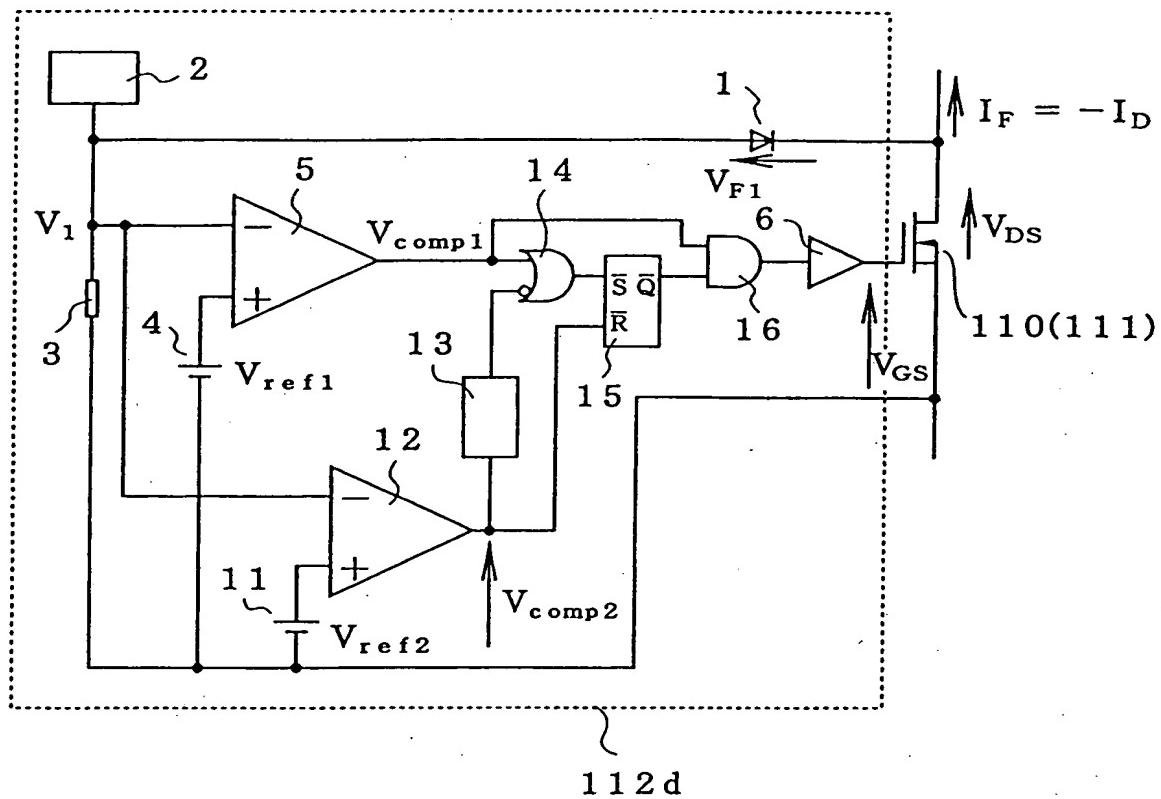


FIG. 11

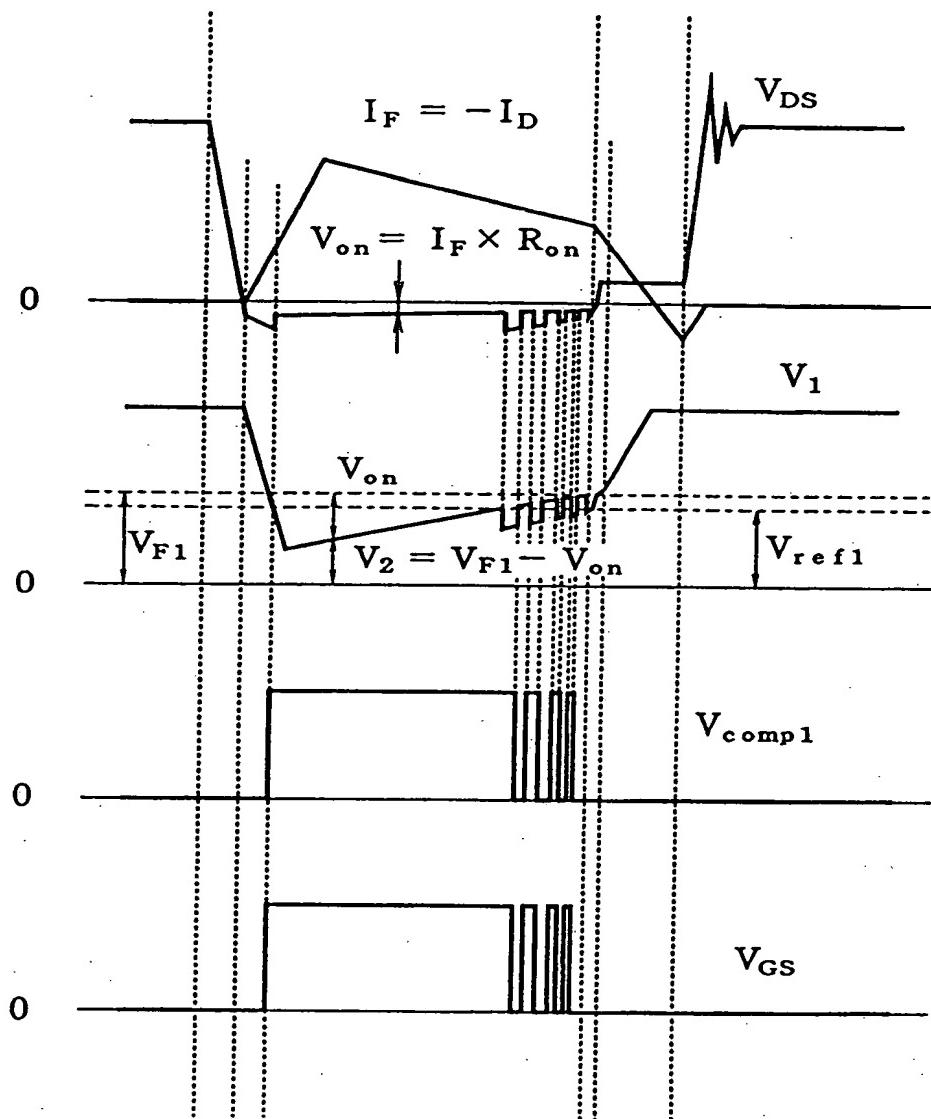


FIG. 12

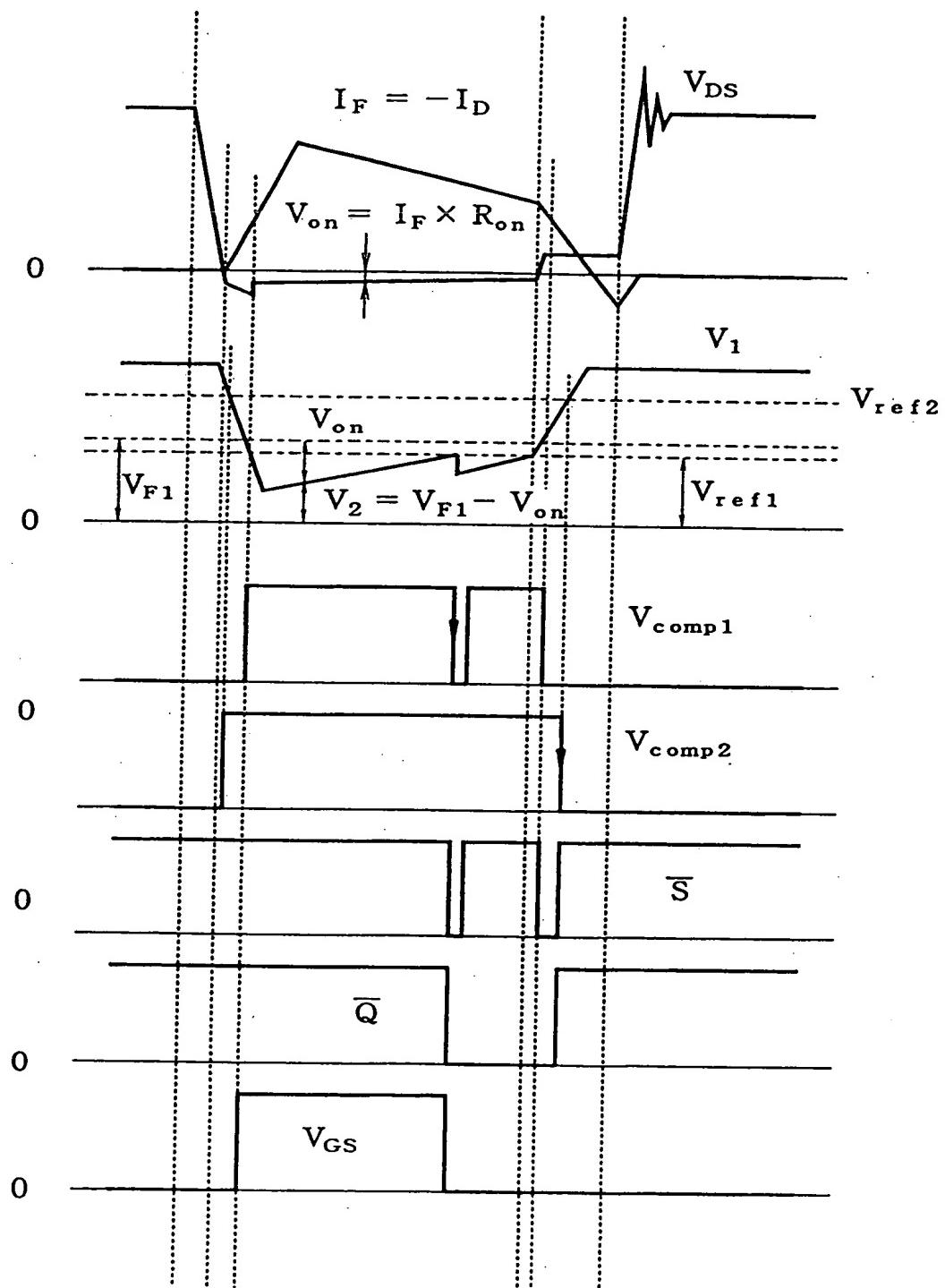


FIG. 13

FIG. 14

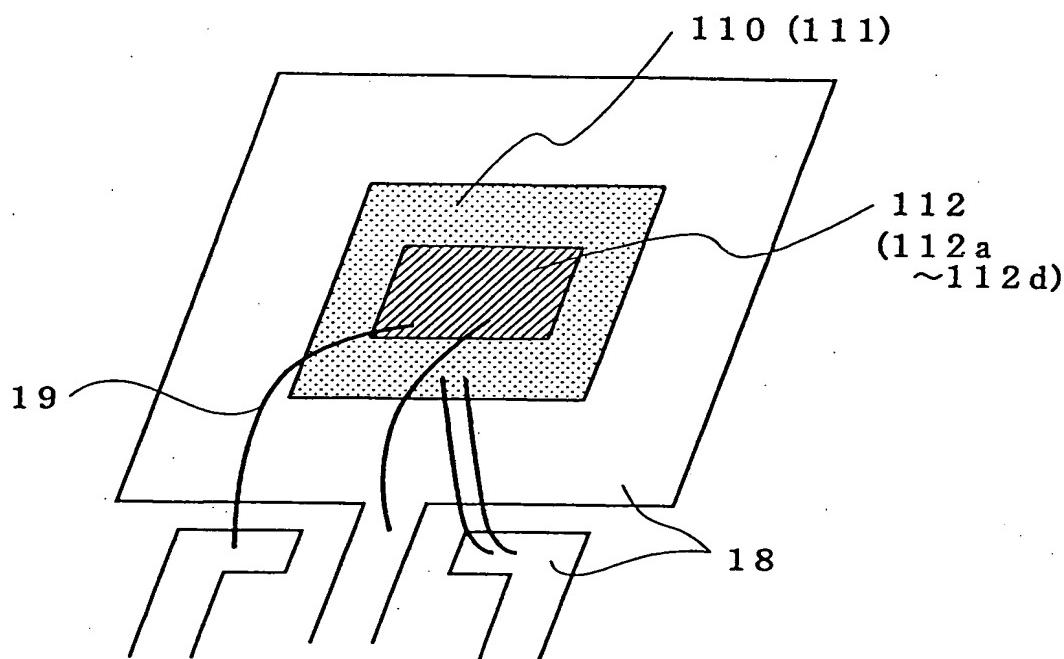
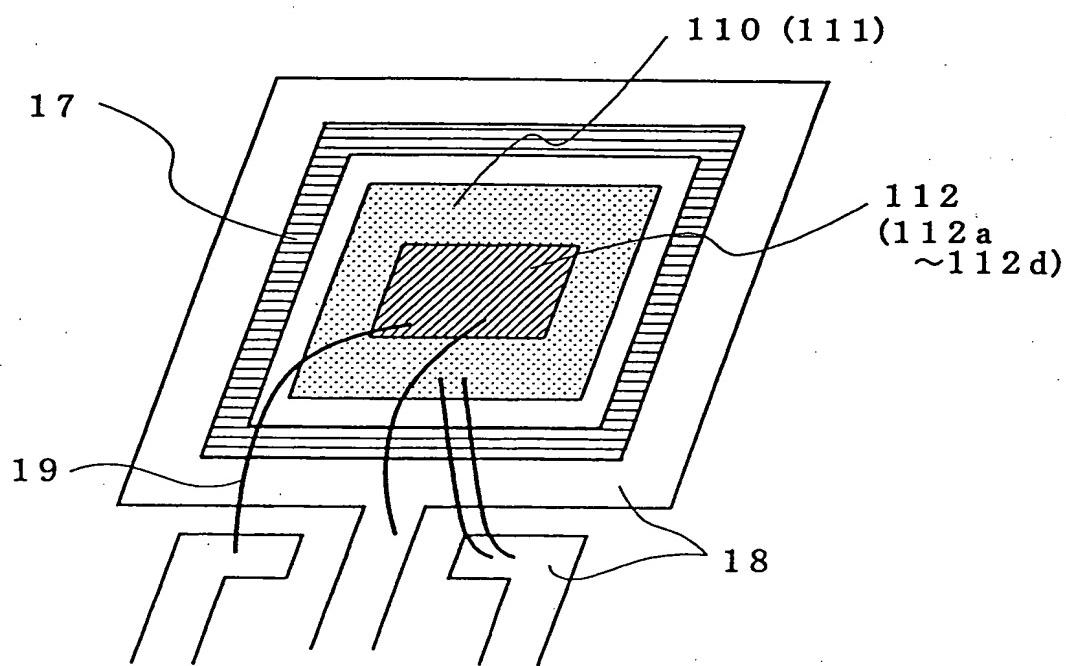
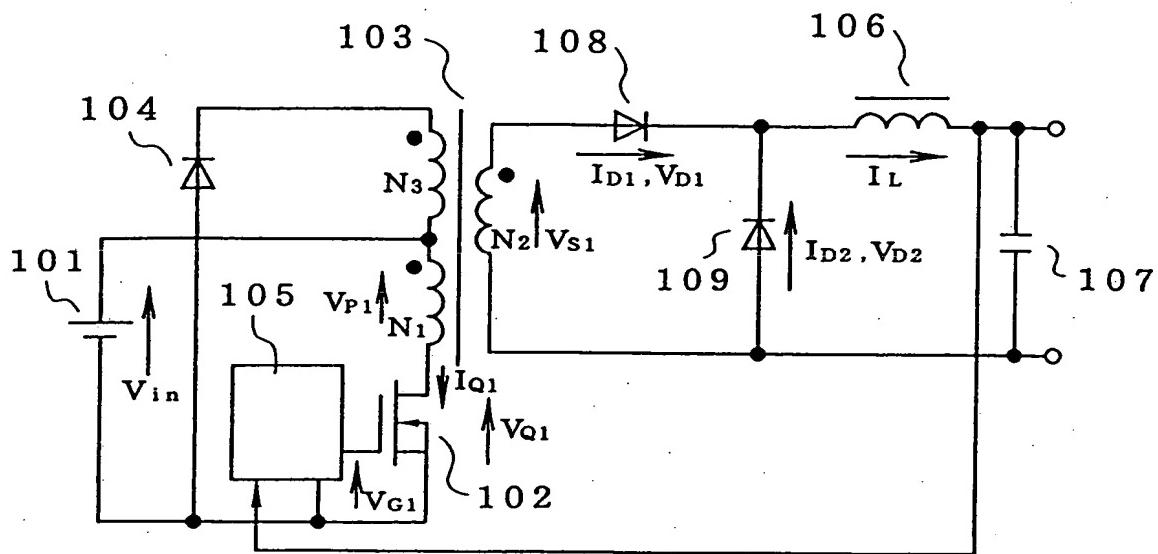
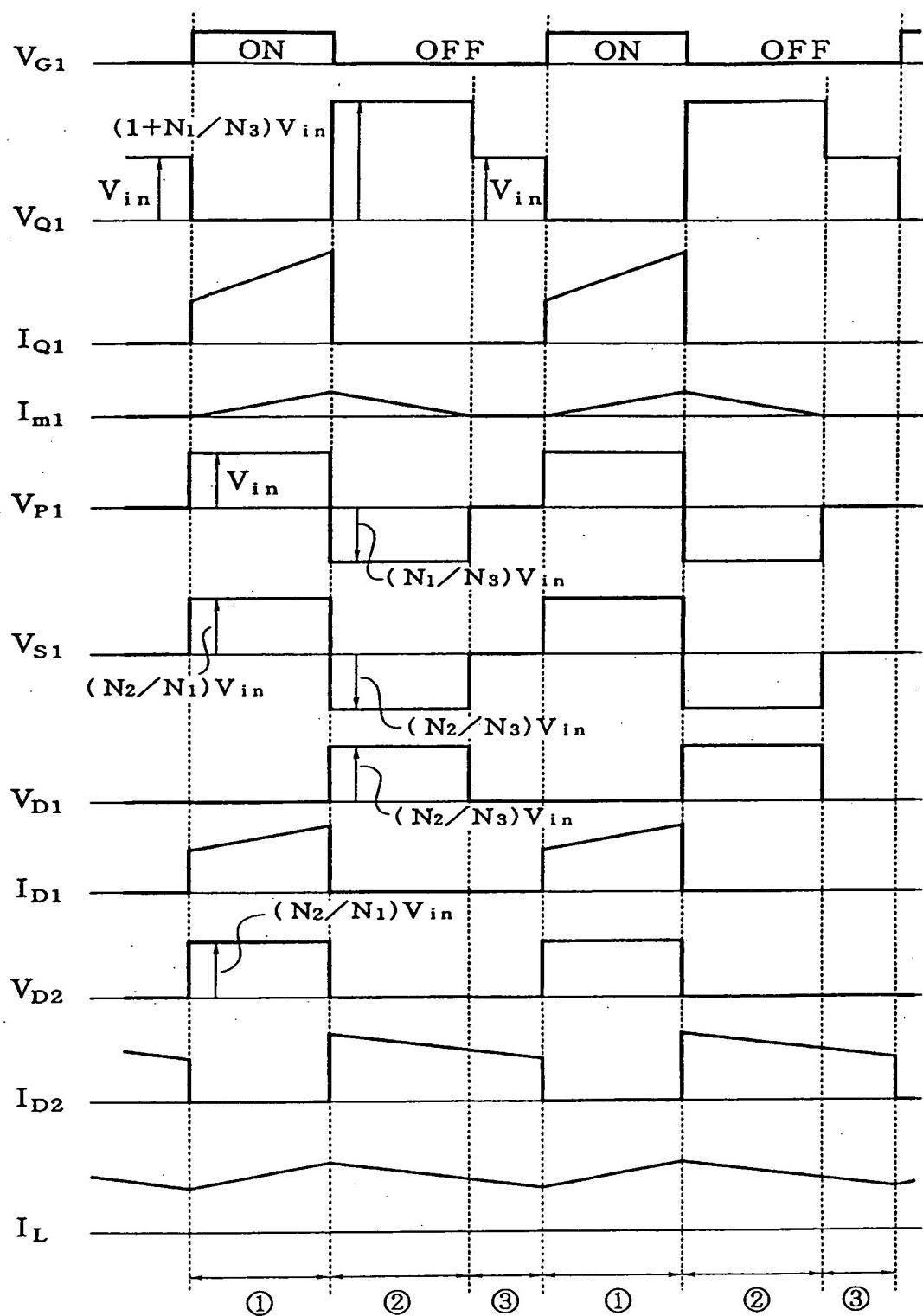


FIG. 15





**FIG. 16**  
**Prior Art**



**FIG. 17**  
**Prior Art**

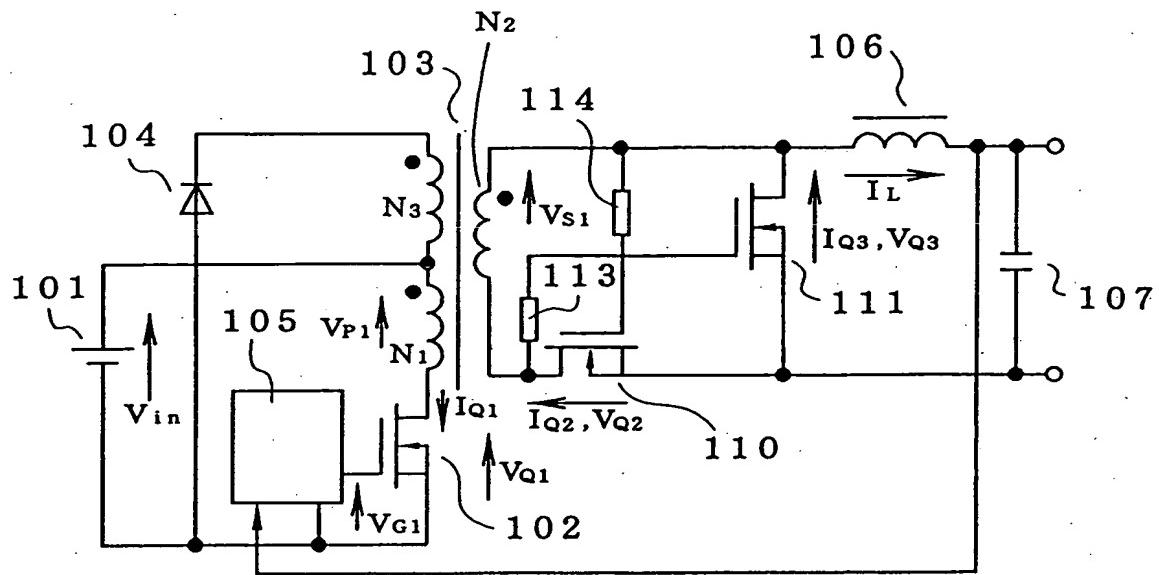
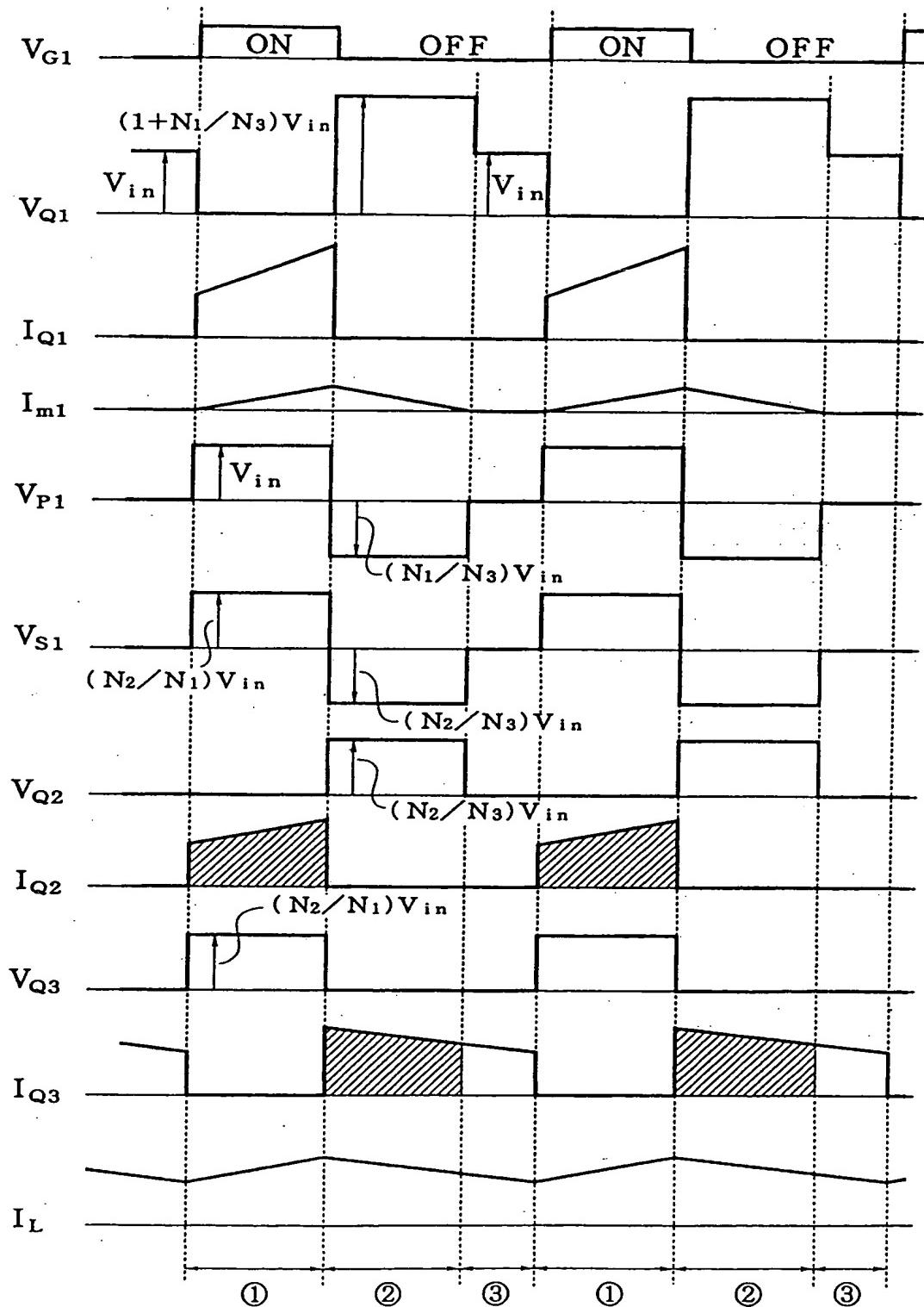


FIG. 18  
Prior Art



**FIG. 19**  
**Prior Art**